

NAIPUNYA: Digital Circuit Debugging

This event aims at developing the ability to design and test the functionality of electronic circuits. It is designed in two different categories, junior level and senior level. Two rounds conducted for the junior level and three rounds are conducted for the senior level, to test their logic skills and knowledge of the subject with proper evaluation and elimination. It also includes design and implementation of puzzle solving circuits which will definitely put the contestant's knowledge, logic, and experience to test. This event aims at testing the logic design and the implementation.

REGISTRATION FEES: 100/- per team

TEAM: Maximum 2 members

PRIZE MONEY:

- Junior: 2k, 1k
- Senior: 3k, 1.5k

GROUPS:

1. Junior level (First year engineering, Second year engineering)
2. Senior level (Third year engineering, Final year engineering)

RULES:

1. Round 1 will be the theory round. Objective and subjective questions based on electronics will be asked to both the groups (junior and senior). Level of questions will vary.
2. The highest scoring teams will be selected for the next round of practical implementation.
3. Problem statement to design an electronic circuit will be given in Round 2. A winner and a runner up will be chosen by the judges from each of the two groups.